

Driving semiconductor growth through digital innovation

Hands-on Workshop on AppliedTwin™ – a cutting-edge digital twin platform for semiconductor equipment and processes.

- » Explore Multiphysics software solutions: **ACE+** and **TOPO+**
- » Understand digital twin technology
- » Dive into live examples in Plasma Enhanced Chemical Vapor Deposition (PECVD) and Etch
- » See how we solve real-world challenges in semiconductor manufacturing applications

Workshop Agenda*:

- » 09:30 – 10:00 : Welcome and Inauguration
- » 10:00 – 10:30 : Overview of Fabrication Process (IITH)
- » 10:30 – 11:00 : Brief Overview of Applied Materials and AppliedTwin™ Framework
- » 11:00 – 11:15 : High Tea
- » 11:15 – 12:05: Tutorial 1: Thermal CVD
- » 12:05 – 13:30 : Tutorial 2: PECVD – Reactor Scale & Feature Scale
- » 13:30 – 14:30 : Lunch Break
- » **Afternoon Sessions**
- » 14:30 – 16:00 : Tutorial 3: Etch — Reactor Scale & Feature Scale
- » 16:00– 16:15 : Valedictory Session
- » * Participants must kindly carry their own laptops



November 4th, 2026



09:30 AM onwards



IIT Hyderabad
Kandi, Sangareddy

Workshop Convenors

Prof. Ranjith Ramadurai

Dr. Chandrashekhara Murapaka

Dept., of Materials Science and Metallurgical Engineering, IIT Hyderabad

Guest Speakers

(Applied Materials India)

Dr. Karthik Ramanathan

Mr. G T Shivakumar

Dr. Kailash Meher

USE QR CODE TO REGISTER

Limited Seats – Sign-up by October 30th, 2026



Whether you're a student, academician, or industry professional, this is your chance to see how digital innovation is shaping the future of semiconductors.